

TO SEPARATE, HOLD TOP AND BOTTOM EDGES, SNAP-APART AND DISCARD CARBON

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <b>08/327887</b>	GROUP/ART UNIT <b>1107</b>	ATTACHMENT TO PAPER NUMBER <b>16</b>	
NOTICE OF REFERENCES CITED				APPLICANT(S) <b>Kim et al</b>			
<b>U.S. PATENT DOCUMENTS</b>							
•	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE	
A	4804560	2/89	Shioya et al	427	125		
B	4956313	9/90	Cote et al	437	195		
C	5063175	11/91	Broadbent	437	195	12/88	
D	5100817	3/92	Catebaum et al	437	245	7/91	
E							
F							
G							
H							
I							
J							
K							
<b>FOREIGN PATENT DOCUMENTS</b>							
•	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG. PP. SPEC.
L							
M							
N							
O							
P							
Q							
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
R	S. Wolf, "Silicon Processing For The VLSI Era; vol. 2", Lattice Press, 1990, pp. 201-205						
S							
T							
U							
EXAMINER <b>Thomas Beldeau</b>				DATE <b>8/31/95</b>			
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)							